



- L1: (83887) S2dram
- L2: (14438) synchroniz\$6 same data same read\$4 same clock
- L3: (2390) 1 and 2
- L4: (830) 1 same 2
- L5: (8) 1 same farthest same controller
- L22: (15) 1 same ((most adj extended) or longest) same controller
- L24: (19) \$3ram same farthest same controller
- L26: (2) \*11053296\*
- L33: (15) \*0205934\*
- L35: (19) (MAESAKO near2 ISATO).in.
- L34: (19) (HIGUCHI near2 HIDEKAZU).in.
- L57: (4818) 365/233
- L58: (206880) 365/
- L59: (1010) 365/191
- L60: (801) 365/193
- L61: (2244) 365/194
- L62: (261) 4 and 57
- L64: (418) 4 and 58
- L66: (98) 4 and 61
- L65: (36) 4 and 60
- L63: (25) 4 and 59
- L67: (2127) 365/51
- L68: (2804) 365/63
- L69: (20) 4 and 67
- L70: (21) 4 and 68
- L72: (575) 3 and 57
- L73: (1024) 3 and 58
- L74: (54) 3 and 59
- L75: (65) 3 and 60
- L76: (211) 3 and 61
- L77: (42) 3 and 67
- L78: (72) 3 and 68
- L79: (22) 77 not 69
- L80: (38) 78 not 77

Q8s USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM; TDB P Plurals

Default operator: OR Highlight all hit terms initially

☐ BRS form 
 ☐ ISAR form 
 ☐ Image 
 ☐ Text 
 ☐ HTML

	U	1	2	Document ID	Issue Date	Page	Title	Current OR	Current XRe	Retrieval C	Inventor	S	C	P	3
1	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 11053296 A	19990226	8	DATA OUTPUT SYNCHRONO				HIGUCHI, HIDEKAZU et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5963502 A	19991005	23	Clock-synchronous type semi	365/233	327/141;		Watanabe, Naoya et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6141265 A	20001031	10	Clock synchronous memory	365/194	365/233		Jeon, Chun Woo	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6359481 B	20020319	15	Data synchronization circuit	327/141	327/34;		Kim, Kyu-hyoun	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>